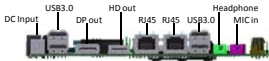
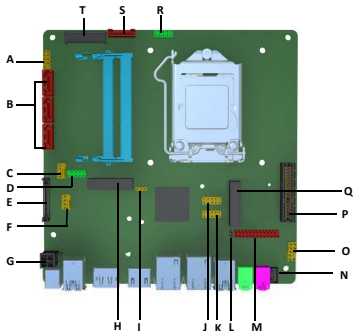


■ Rear I/O Placement



■ On-board I/O Placement



Item	Description
A	Front panel connector
B	SATA III signal connector
C	CPU fan header
D	Dual USB2.0 header
E	SATA power connector
F	System fan header
G	DC Power header(19V)
H	mSATA/ Mini PCIE slot
I	CMOS jumper
J	Serial header
K	Serial header
L	AT/ATX jumper
M	Parallel header
N	AMP SPKR connector
O	Front Audio
P	PCI Express X4 slot
Q	Mini PCIE slot (Half Length)
R	Dual USB2.0 header
S	Backlight (Brightness control)
T	LVDS connector

■ Front panel connector

Signal	Pin	Pin	Signal
HD_LED+	1	2	PWR_LED+
HD_LED-	2	4	PWR_LED-
GND	5	6	PWR_BTN
RESET	7	8	GND
NC	9	10	[KEY]

■ SATA III signal connector

Signal Name	Description
GND	Ground
SATAHDR_TXPO_C	SATA DATA Transmit(positive)
SATAHDR_TXNO_C	SATA DATA Transmit(negative)
GND	Ground
SATAHDR_RXNO_C	SATA DATA Receive(negative)
SATAHDR_RXPO_C	SATA DATA Receive(positive)
GND	Ground
G1	NC
G2	NC

■ Dual USB2.0 header

Signal	Pin	Pin	Signal
5V_USB	1	2	5V_USB
DATA-	2	4	DATA-
DATA+	5	6	DATA+
GND	7	8	GND
NC	9	10	[KEY]

■ SATA power connector

Signal Name	Description
VCC3	Power
VCC3	Power
VCC3	Power
GND	Ground
GND	Ground
GND	Ground
VCC	Power
VCC	Power
VCC	Power
GND	Ground
RES	NC
GND	Ground
+12V	Power
+12V	Power
+12V	Power

■ Fan header

Signal
Ground
+12V
CPU_FAN_TACH
CPU_FAN_CTRL

■ CMOS jumper

Pin	Description
1-2	Clear CMOS
2-3	Normal (Default)

Serial header

Pin	Signal Name
1	DCD
2	RXD#
3	TXD#
4	DTR
5	GND
6	DSR
7	RTS
8	CTS
9	RI
10	Key

AT/ATX Jumper

Pin	Signal Name
1-2	AT
2-3	ATX (Default)

AMP SPKR connector

Pin	Signal Name
1	Front_L-
2	Front_L+
3	Front_R+
4	Front_R-

Parallel header

Pin	Standard Signal Name	ECP Signal Name	EPP Signal Name
1	STROBE#	STROBE#	WRITE#
2	AUTOFD#	AUTOFD#, HOSACK	DATASTB#
3	PD0	PD0	PD0
4	FAULT#	FAULT#, PERIPHERQST#	FAULT#
5	PD1	PD1	PD1
6	INT#	INT#, REVERSERQST#	RESET#
7	PD2	PD2	PD2
8	SLCTIN#	SLCTIN#	ADDRSTB#
9	PD3	PD3	PD3
10	GROUND	GROUND	GROUND
11	PD4	PD4	PD4
12	GROUND	GROUND	GROUND
13	PD5	PD5	PD5
14	GROUND	GROUND	GROUND
15	PD6	PD6	PD6
16	GROUND	GROUND	GROUND
17	PD7	PD7	PD7
18	GROUND	GROUND	GROUND
19	ACK#	ACK#	INTR
20	GROUND	GROUND	GROUND
21	BUSY	BUSY#, PERIPHACK	WAIT#
22	GROUND	GROUND	GROUND
23	PERROR	PE, ACKREVERSE#	PE
24	GROUND	GROUND	GROUND
25	SELECT	SELECT	SELECT
26	KEY (no pin)	KEY (no pin)	KEY (no pin)

■ Front Audio

Pin	Signal Name	Description
1	FP_MIC_L	MIC Left channel
2	AGND	GND
3	FP_MIC_R	MIC Right channel
4	F_AUDIO_DET_N	Audio insertion detection
5	FP_HPOUT_R	HP out Right channel
6	AUD_SENSE_MIC_FP	MIC insertion detection
7	FIO_SENSE	FIO detection
8	Key	No pin
9	FP_HPOUT_L	HPOUT left channel
10	AUD_SENSE_HP	HP detection

■ Dual USB2.0 header

Pin	Signal	Signal
1	5V_USB	5V_USB
3	Data (negative)	Data (negative)
5	Data (positive)	Data (positive)
7	Ground	Ground
9	Key (no pin)	No Connect

■ Backlight control

Pin	Signal Name
1	BKLT_PWR_12V
2	GND
3	BKLT_EN
4	BRIGHT_CTRL
5	LCD_5V

■ LVDS connector

Signal	Pin	Pin	Signal
+3.3V	1	2	+5V
+3.3V	3	4	+5V
DDC_SCL	5	6	DDC_SDA
GND	7	8	GND
LVDS_A1+	9	10	LVDS_A0+
LVDS_A1-	11	12	LVDS_A0-
GND	13	14	GND
LVDS_A3+	15	16	LVDS_A2+
LVDS_A3-	17	18	LVDS_A2-
GND	19	20	GND
LVDS_B1+	21	22	LVDS_B0+
LVDS_B1-	23	24	LVDS_B0-
GND	25	26	GND
LVDS_B3+	27	28	LVDS_B2+
LVDS_B3-	29	30	LVDS_B2-
GND	31	32	GND
LVDS_B_CLK+	33	34	LVDS_A_CLK+
LVDS_B_CLK-	35	36	LVDS_A_CLK-
GND	37	38	GND
+12V	39	40	+12V